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Silicon wafer are cleaned by a solvent clean, Followed by a dionized water (DI) rinse, followed by an RCA clean and DI rinse, followed by an HF dip and DI rinse and blow dry. This is a level-1 process and requires basic INRF safety certification. ... Handbook of Semiconductor Cleaning Technology, Noyes Publishing: Park Ridge, NJ, 1993 Ch 1.

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498 Handbook of Semiconductor Wafer Cleaning Technology electrical contacts, thus eliminating

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any additional processing steps. It can be used with a bare wafer surface or with dielectric coatings  
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Prior to forming a consulting company, Karen was employed at Novellus Systems, AMD, and Cypress Semiconductor. She has published more than 30 technical publications, has been awarded seven patents, and is co-editor of Handbook of Silicon Wafer Cleaning Technology.

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